

xxx.xPrj
Obj. Export As

Design
Text
X = 276,7 Y = 104,2

Find
Inline
Parsers
Timers
Shapes
Controls
Tools
Reports
Edits
Instruments
Communication
Animations
Effects
3D Objects
Database
Language
About

LD Micro

Read / Write Contact --[]--
Read / Write Coil --()--
Read / Write Variable

Objects
Find
inaccessibles

GaugeC2
CheckBoxP1
ButtonP1
GaugeC1
Image1
Label10
Path1

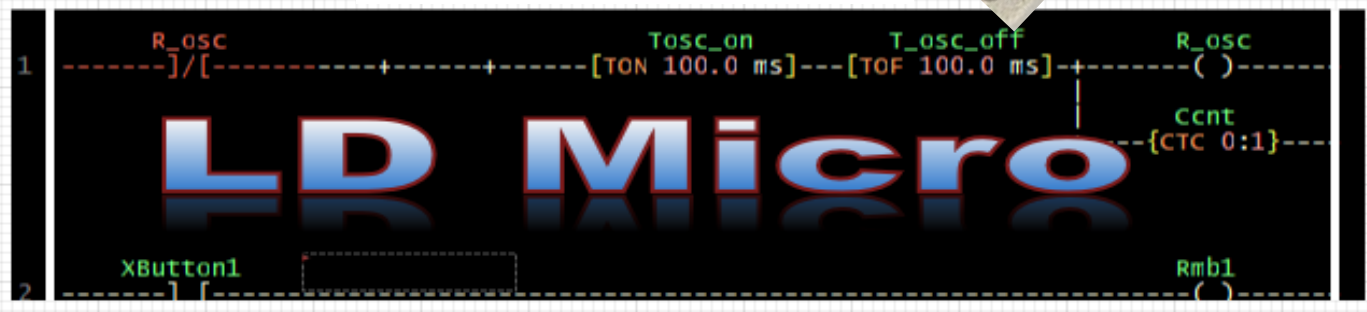
Properties
Links & Referages
Log

Align
ClipChildren
Data
Edit
Enabled
Fill
Hint
HitTest
Locked
Margins
Name
Opacity
Padding
PathSelect

None
False
<TPathData>
False
True
<TBrush>
True
False
<TBounds>
Path1
1
<TBounds>
True

Path1 TXPath

Erol CALISKAN
Vulkan V7
www.micset.net
2008-2021



3

Settings Instruction Simulate Compile Config Help

Microcontroller > Atmel AVR MCUs > Atmel AVR ATmega2560 100-TQFP

MCU Parameters... Ctrl+F5 Microchip Pic10-16 MCUs > Atmel AVR AT90USB647 64-TQFP

Set Pull-up input resistors Microchip Pic18 MCUs > Atmel AVR ATmega128 64-TQFP

Microcontrollers: TODO and DONE > ARM MCUs > Atmel AVR ATmega64 64-TQFP

ESP MCUs > Atmel AVR ATmega162 40-PDIP

Other MCUs > Atmel AVR ATmega32U4 44-Pin packages

(no microcontroller) Atmel AVR ATmega32 44-Pin packages

Atmel AVR ATmega32 40-PDIP

Atmel AVR ATmega16 40-PDIP

Atmel AVR ATmega48 28-PDIP

Atmel AVR ATmega88 28-PDIP

Atmel AVR ATmega168 28-PDIP

Atmel AVR ATmega328 28-PDIP

Atmel AVR ATmega328 32-Pin packages

Atmel AVR ATmega164 40-PDIP

Atmel AVR ATmega324 40-PDIP

PLC Configuration

PLC Cycle Time (ms): 10,000 Timer0|1: 1 ☐ YPlcCycleDuty OK

MCU Crystal Frequency (MHz): 16,000,000 Cancel

UART Baud Rate (bps): 9600 PIC Configuration Bits: 0

SPI Rate (Hz): 0 I2C Rate (Hz): 0

Available PLC MCU PLC Tim 000000000000 In fact TCycle TON,TOF,RTC TON,TOF,RTC TON,TOF,RTC TON,TOF,RTC Serial (UART) Frame format The cycle time cycle times may not ms will usually The compiler timing in clock speed grade before choosing

About LDmicro

ABOUT LDMICRO

LDmicro is a ladder logic editor, simulator and compiler for 8-bit microcontrollers. It can generate native code for Atmel AVR and Microchip PIC16 CPUs from a ladder diagram.

This program is free software: you can redistribute it and/or modify it under the terms of the GNU General Public License as published by the Free Software Foundation, either version 3 of the License, or (at your option) any later version.

This program is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU General Public License for more details.

You should have received a copy of the GNU General Public License along with this program. If not, see <<http://www.gnu.org/licenses/>>.

The source code for LDmicro is available at <http://cq.cx/ladder.pl>

Copyright 2005-2016 Jonathan Westhues
Email: user.jwesthues, at host cq.cx

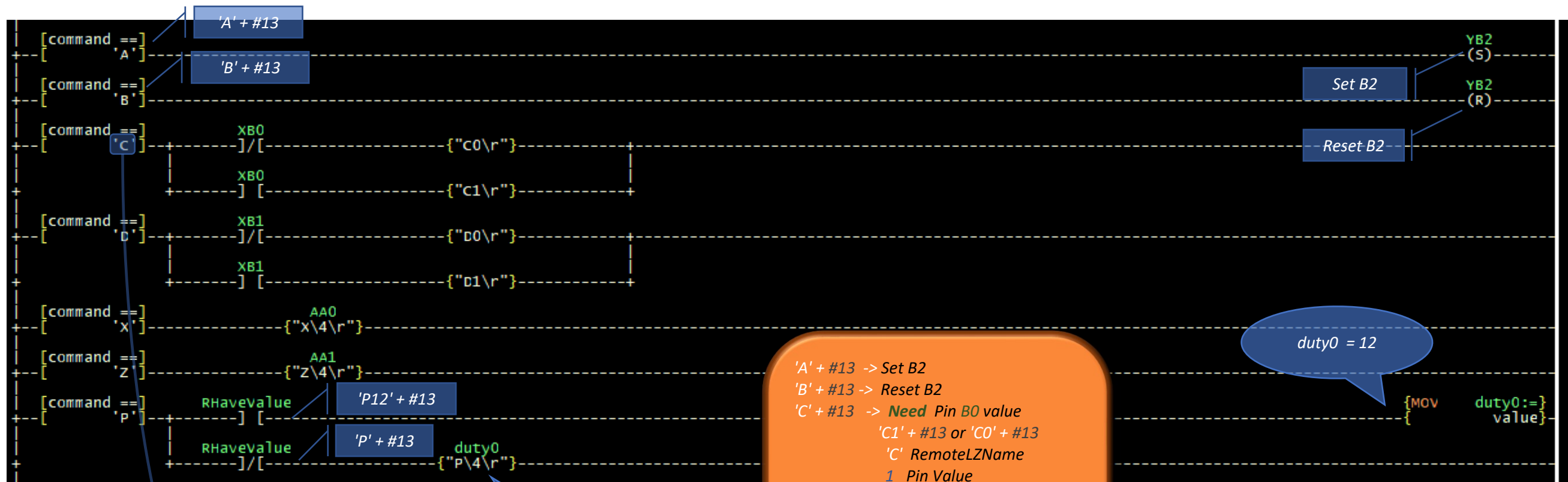
Netzer extension by Sven Schlender (C) 2012
<http://www.mobacon.de/wiki/doku.php/en/netzer/index>

Controllino Maxi support 2016
Frederic Rible <frible@teaser.fr>

ARM 32 bits support, SPI & I2C (C) 2019
Jose GILLES <UCP@France>
Repository: <https://github.com/joegil95>

LDmicro support:
Repository: <https://github.com/LDmicro/LDmicro>
Email: LDmicro.GitHub@gmail.com

Release 5.4.1.1, built 21:01:03 Mar 22 2021.

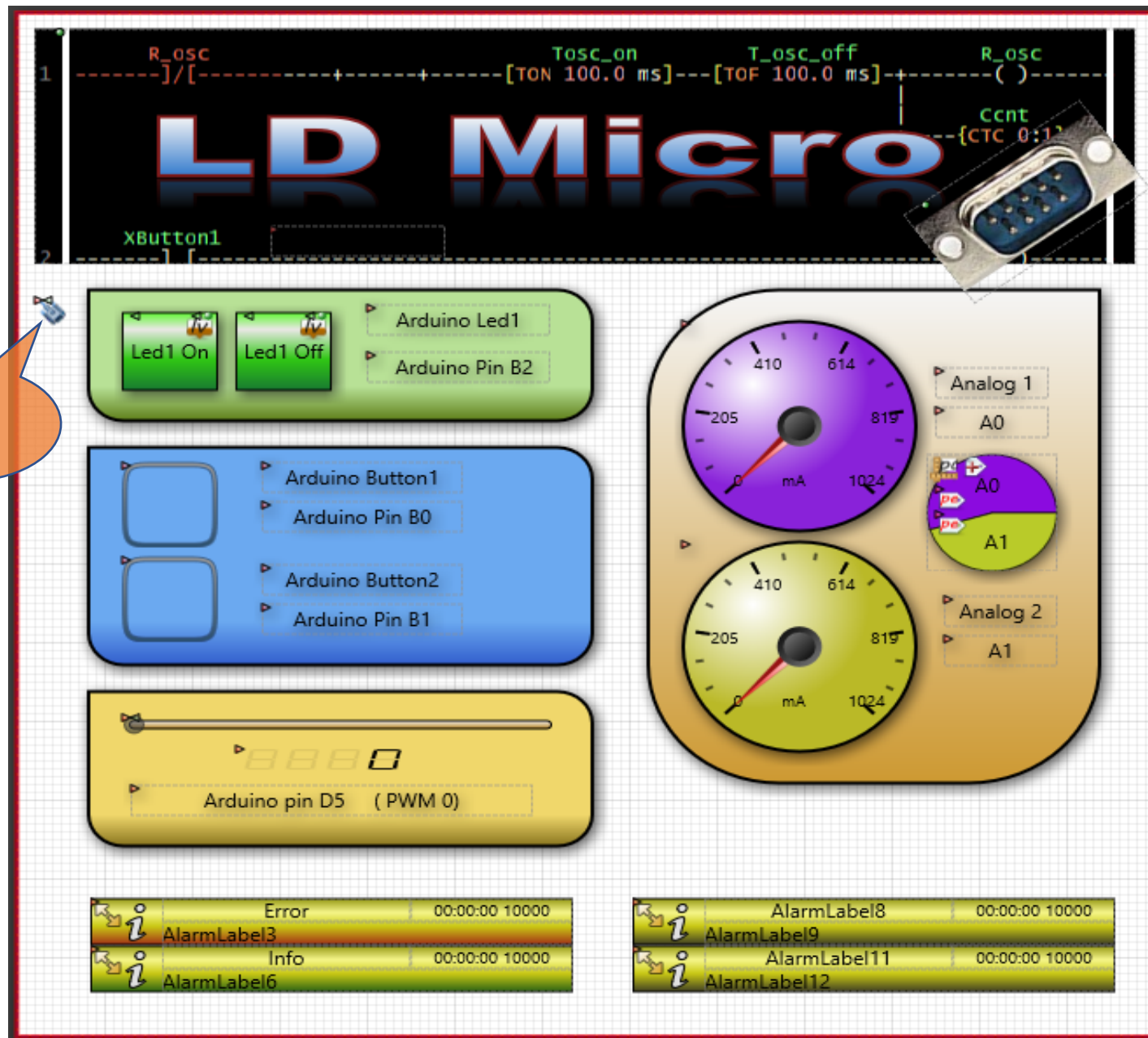


Name	LZTag1
Position	<TPosition>
ReadType	rtAlways
RemoteLZName	C

'P12'+#13

'A' + #13 -> Set B2
 'B' + #13 -> Reset B2
 'C' + #13 -> Need Pin B0 value
 'C1' + #13 or 'C0' + #13
 'C' RemoteLZName
 1 Pin Value
 'D' + #13 -> Need Pin B0 value
 'D1' + #13 or 'D0' + #13
 'D' RemoteLZName
 0 Input value
 'X' + #13 -> Need A0 value
 'X0123' + #13
 'X' RemoteLZName
 0123 - Analog value
 'Z' + #13 -> Need A1 value
 'Z0123' + #13
 'Z' RemoteLZName
 0123 - Analog value
 'P' + #13 -> Need PWM value
 'P0123' + #13
 'P' RemoteLZName
 0123 - PWM value
 'P'12 + #13 -> Set PWM value
 'P12' + #13
 'P' RemoteLZName

duty0 = 12



AlarmError	<?>
AlarmInfo	<?>
AutoConnect	True
BaudRate	9600
DataBits	dbEight
LineEnd	#13
Name	SerialPort1
ParityBits	prNone
Port	COM1
Position	<TPosition>
StopBits	sbOneStopBit
Visible	True

A callout bubble labeled '#13 = "\n"' points to the 'LineEnd' field.

Objects	Find	Invisibles
LangLinkValues8		
LangLinkValues1		
LangLinkValues2		
LangLinkValues3		
LangLinkValues4		
LZClient1		
LZTag1		

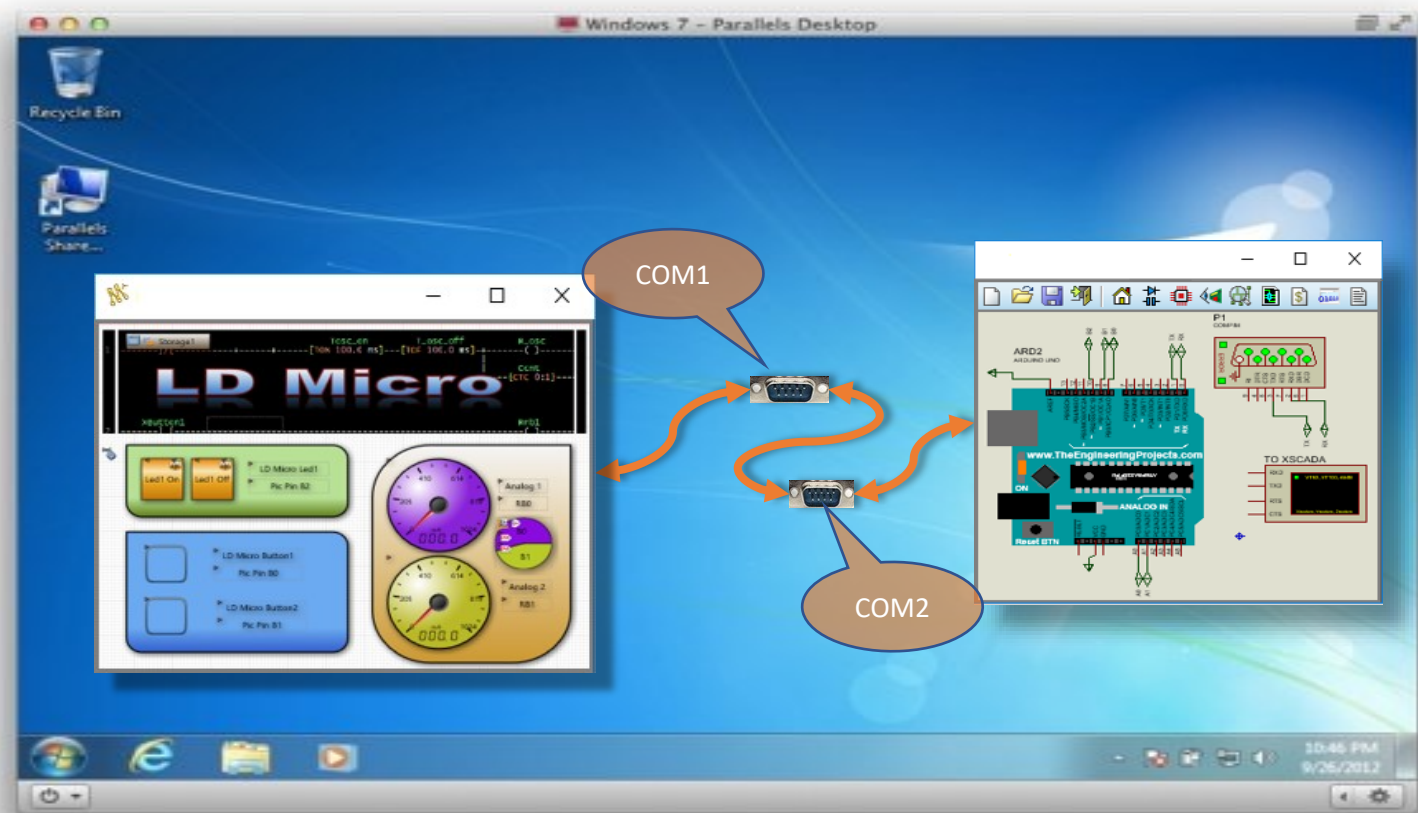
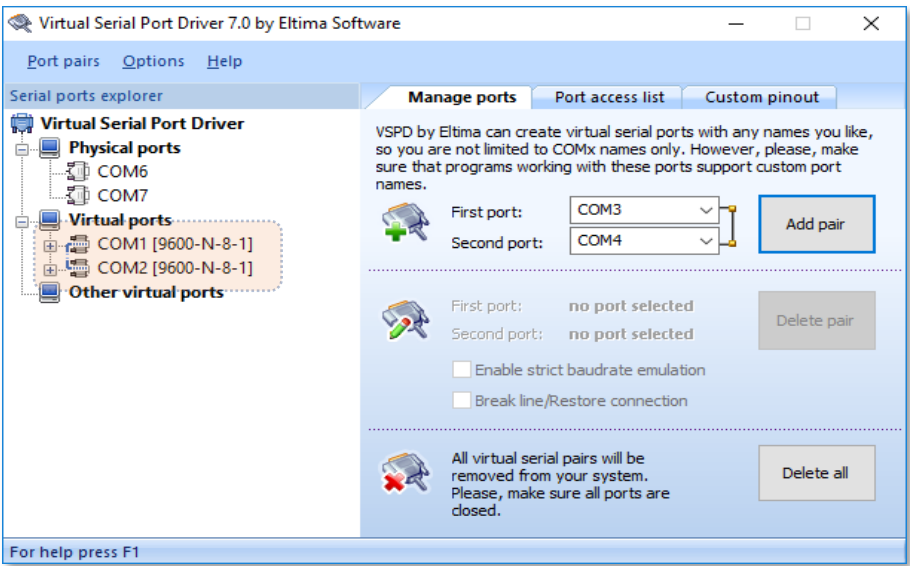
Properties	Links & Referages
Name	LZTag1
Position	<TPosition>
ReadType	rtAlways
RemoteLZName	C
Value	

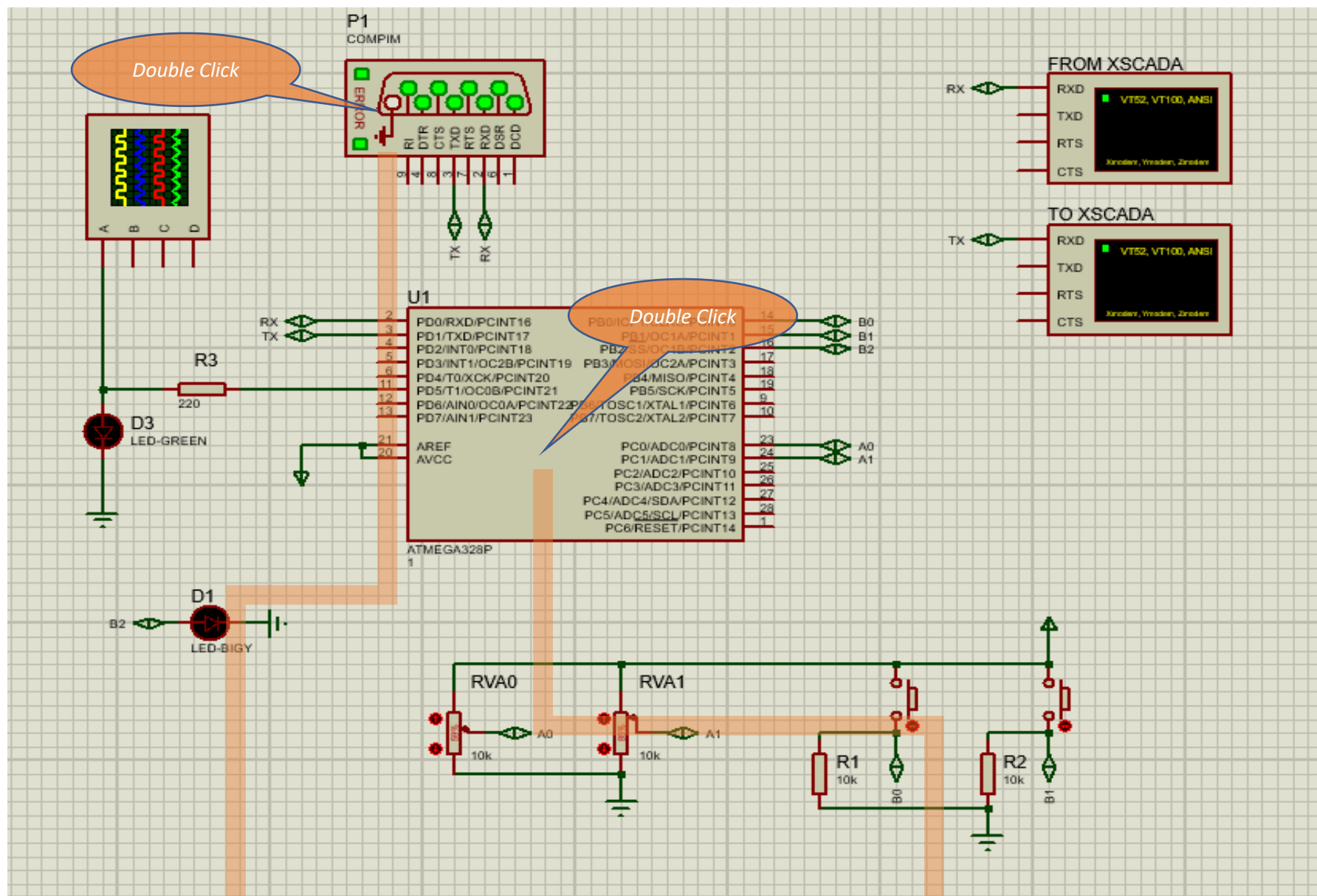


Visual Link

LZ Protocol

LZ Tag





Edit Component

Part Reference: Hidden: ☐

Part Value: Hidden: ☐

Element: New

VSM Model: Hide All

Physical port: Hide All

Physical Baud Rate: Hide All

Physical Data Bits: Hide All

Physical Parity: Hide All

Virtual Baud Rate: Hide All

Virtual Data Bits: Hide All

Virtual Parity: Hide All

Advanced Properties:

Physical Stop Bits: Hide All

Other Properties:

☐ Exclude from Simulation ☐ Attach hierarchy module

☐ Exclude from PCB Layout ☐ Hide common pins

☐ Exclude from Current Variant ☐ Edit all properties as text

Edit Component

Part Reference: Hidden: ☐

Part Value: Hidden: ☐

Element: New

PCB Package: Hide All

Program File: Hide All

CLKDIV8 (Divide clock by 8): Hide All

CKOUT (Clock output): Hide All

RSTDISBL (External reset disable): Hide All

WDTON (Watchdog Timer Always On): Hide All

BOOTSRT (Select reset vector): Hide Name

CKSEL Fuses: Hide All

Boot Loader Size: Hide All

SUT Fuses: Hide All

Advanced Properties:

Clock Frequency: Hide All

Other Properties:

☐ Exclude from Simulation ☐ Attach hierarchy module

☐ Exclude from PCB Layout ☐ Hide common pins

☐ Exclude from Current Variant ☐ Edit all properties as text

OK Help Data Hidden Pins Edit Firmware Cancel