

xxx.xPrj

Obj. Import

Obj. Export As

Find

Inline

Parsers

Timers

Shapes

Controls

Tools

Reports

Edits

Instruments

Communication

Animations


Effects

3D Objects

Database

Language

About



EroI CALISKAN

Vulkan V7

www.micset.net

2008-2021

Design

Text

X = 285,0 Y = 97,5


X 10

LD Micro

Read / Write Contact --[]--

Read / Write Coil --()--

Read Variable



Objects

Find

inaccessibles

GaugeC2

CheckBoxP1

ButtonP1

GaugeC1

Image1

Label10

Path1

Properties

Links & Referages

Log

Align

ClipChildren

Data

Edit

Enabled

Fill

Hint

HitTest

Locked

Margins

Name

Opacity

Padding

PathSelect

Position

None

False

<TPathData>

False

True

<TBrush>

True

False

<TBounds>

Path1

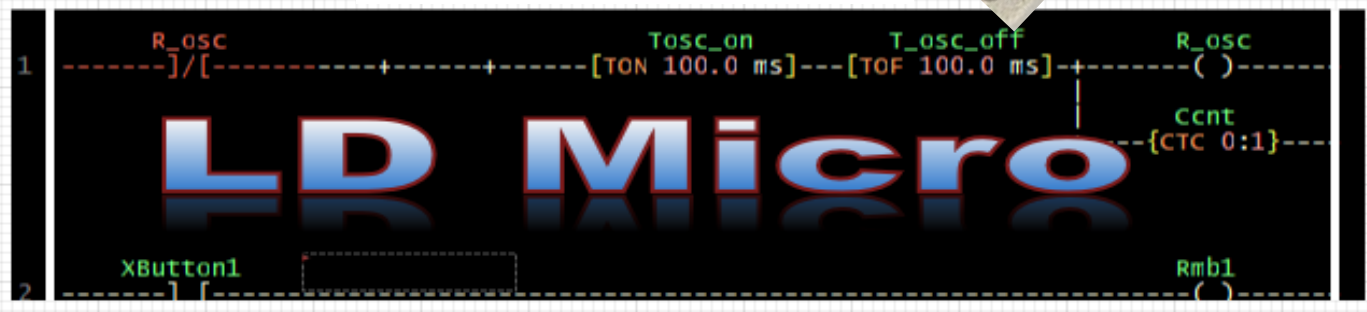
1

<TBounds>

True

<TPosition>

Path1 TXPath



3

Settings Instruction Simulate Compile Config Help

Microcontroller > Atmel AVR MCUs > Atmel AVR ATmega2560 100-TQFP

MCU Parameters... Ctrl+F5 Microchip Pic10-16 MCUs > Atmel AVR AT90USB647 64-TQFP

Set Pull-up input resistors Microchip Pic18 MCUs > Atmel AVR ATmega128 64-TQFP

Microcontrollers: TODO and DONE > ARM MCUs > Atmel AVR ATmega64 64-TQFP

ESP MCUs > Atmel AVR ATmega162 40-PDIP

Other MCUs > Atmel AVR ATmega32U4 44-Pin packages

(no microcontroller) Atmel AVR ATmega32 44-Pin packages

Atmel AVR ATmega32 40-PDIP

Atmel AVR ATmega16 40-PDIP

Atmel AVR ATmega48 28-PDIP

Atmel AVR ATmega88 28-PDIP

Atmel AVR ATmega168 28-PDIP

Atmel AVR ATmega328 28-PDIP

Atmel AVR ATmega328 32-Pin packages

Atmel AVR ATmega164 40-PDIP

Atmel AVR ATmega324 40-PDIP

PLC Configuration

PLC Cycle Time (ms): 10,000 Timer0|1: 1 ☐ YPlcCycleDuty OK

MCU Crystal Frequency (MHz): 16,000,000 Cancel

UART Baud Rate (bps): 9600 PIC Configuration Bits: 0

SPI Rate (Hz): 0 I2C Rate (Hz): 0

Available PLC MCU PLC Tim 000000000000 In fact TCycle TON,TOF,RTC TON,TOF,RTC TON,TOF,RTC TON,TOF,RTC

Serial (UART) Frame format

The cycle time cycle times may not ms will usually

The compiler timing in clock speed grade before choosing

ABOUT LDMICRO

Ldmicro is a ladder logic editor, simulator and compiler for 8-bit microcontrollers. It can generate native code for Atmel AVR and Microchip PIC16 CPUs from a ladder diagram.

This program is free software: you can redistribute it and/or modify it under the terms of the GNU General Public License as published by the Free Software Foundation, either version 3 of the License, or (at your option) any later version.

This program is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU General Public License for more details.

You should have received a copy of the GNU General Public License along with this program. If not, see <<http://www.gnu.org/licenses/>>.

The source code for Ldmicro is available at <http://cq.cx/ladder.pl>

Copyright 2005-2016 Jonathan Westhues  
Email: [user.jwesthues@host.cq.cx](mailto:user.jwesthues@host.cq.cx)

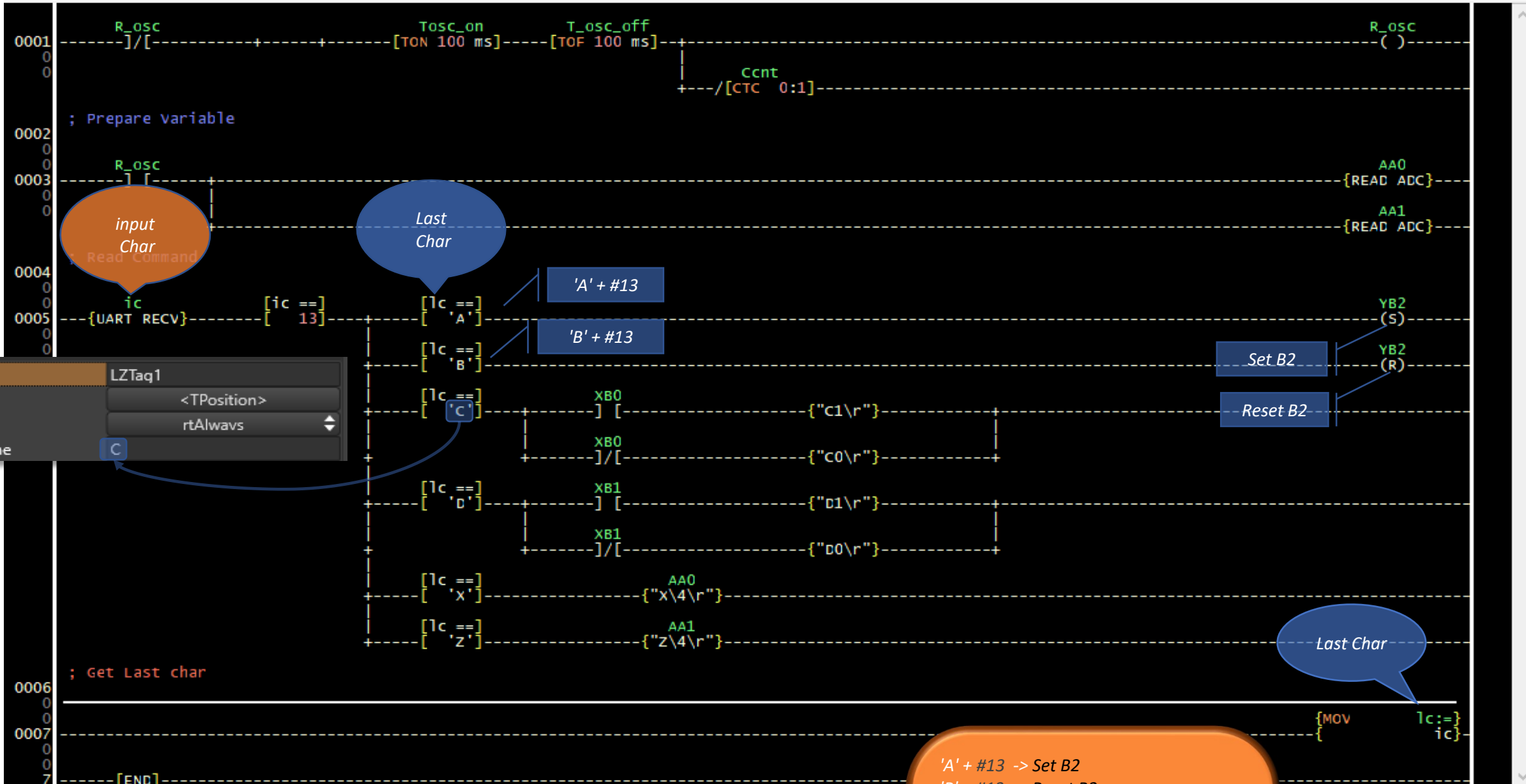
Netzer extension by Sven Schlender (C) 2012  
<http://www.mobacon.de/wiki/doku.php/en/netzer/index>

Controllino Maxi support 2016  
Frederic Rible <[frible@teaser.fr](mailto:frible@teaser.fr)>

ARM 32 bits support, SPI & I2C (C) 2019  
Jose GILLES <[UCP@France](mailto:UCP@France)>  
Repository: <https://github.com/joegil95>

Ldmicro support:  
Repository: <https://github.com/LDmicro/LDmicro>  
Email: [LDmicro.GitHub@gmail.com](mailto:LDmicro.GitHub@gmail.com)

Release 5.4.1.1, built 21:01:03 Mar 22 2021.



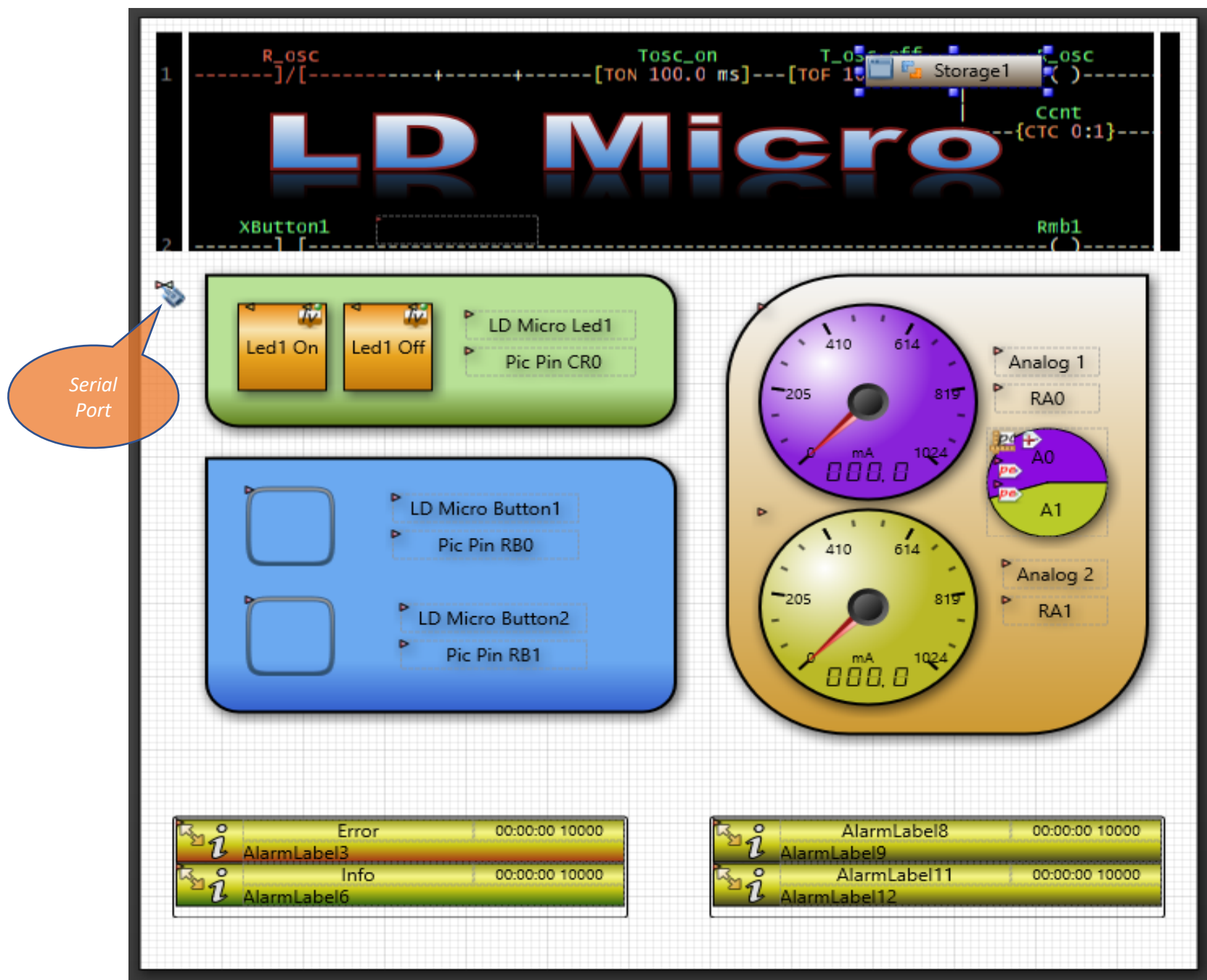
Name	LZTaql
Position	<TPosition>
ReadType	rtAlways
RemoteLZName	C

Name	Type	State	Pin on M...	MCU P...	Pin Name
ic	general var	0x0000 = 0			
lc	general var	0x0000 = 0			
Ccnt	counter	0x0000 = 0			
XB0	digital in	0	14	PB0	PB0 PCINT0/CLKO/ICP1
XB1	digital in	0	15	PB1	PB1 OC1A/PCINT1
YB2	digital out	0	16	PB2	PB2_SS/OC1B/PCINT2
AA0	adc input	0x0000 = 0	23	PC0	PC0 ADC0/PCINT8
AA1	adc input	0x0000 = 0	24	PC1	PC1 ADC1/PCINT9
AA0	UART tx	0x0000 = 0	3	PD1	PD1 PCINT17/TXD
AA1	UART tx	0x0000 = 0	3	PD1	PD1 PCINT17/TXD
ic	UART rx	0x0000 = 0	2	PD0	PD0 PCINT16/RXD
R_osc	int. relay	0			
Tosc_on	turn-on delay	0x0000 = 0 = 0 ms			
T_osc_off	turn-off delay	0x0000 = 0 = 0 ms			

Atmel AVR ATmega328 28-PDIP    processor clock 16 MHz    Tcycle= 10 ms F= 100 Hz F/2= 50 Hz

'A' + #13 -> Set B2  
 'B' + #13 -> Reset B2  
 'C' + #13 -> **Need** Pin B0 value  
                   'C1' + #13 or 'C0' + #13  
                   'C' RemoteLZName  
                   1 Pin Value  
 'D' + #13 -> **Need** Pin B0 value  
                   'D1' + #13 or 'D0' + #13  
                   'D' RemoteLZName  
                   0 Input value  
 'X' + #13 -> **Need** A0 value  
                   'X0123' + #13  
                   'X' RemoteLZName  
                   0123 - Analog value  
 'Z' + #13 -> **Need** A1 value  
                   'Z0123' + #13  
                   'Z' RemoteLZName





AlarmError	<?>
AlarmInfo	<?>
AutoConnect	True
BaudRate	9600
DataBits	dbEight
LineEnd	#13
Name	SerialPort1
ParityBits	orNone
Port	COM1
Position	<TPosition>
StopBits	sbOneStopBit
Visible	True

#13 = '\n'

Objects	Find	Invisibles
LangLinkValues8		
LangLinkValues1		
LangLinkValues2		
LangLinkValues3		
LangLinkValues4		
LZClient1		
LZTag1		
Properties	Links & Referages	
Name	LZTag1	
Position	<TPosition>	
ReadType	rtAlways	
RemoteLZName	C	
Value		



Visual Link

LZ Protocol

LZ Tag

