



PlcMTag Component

Using :
Mod Bus Tag Contains Register information

Events :
OnChangeValue After Change Register Value
OnReadValue After Read Register Value

Propertyes :
RegType tgBoolean , tgUint16 , tgUint32 , tgSint16 , tgSint32 , tgFloat32
tgBoolean BITS 0..1 c++ Bit
tgUint16 WORD 0 .. 65535 c++ unsigned short
tgUint32 DWORD 0 .. 4294967295 c++ unsigned int
tgSint16 INT -32768 .. 32767 c++ short
tgSint32 DINT -2147483648 .. 2147483647 c++ int
tgFloat32 REAL float point c++ float

RegFunction fnHoldingReg , fnInputReg , fnInputBit , fnCoilBit

ReadType rtAlways , rtOnTime , rtOnTimeOrAfterWrite , rtAfterWrite , rtNone
rtAfterWriteOrReadDelay , rtAfterWriteOrDelayRead , rtReadDelay , rtDelayRead

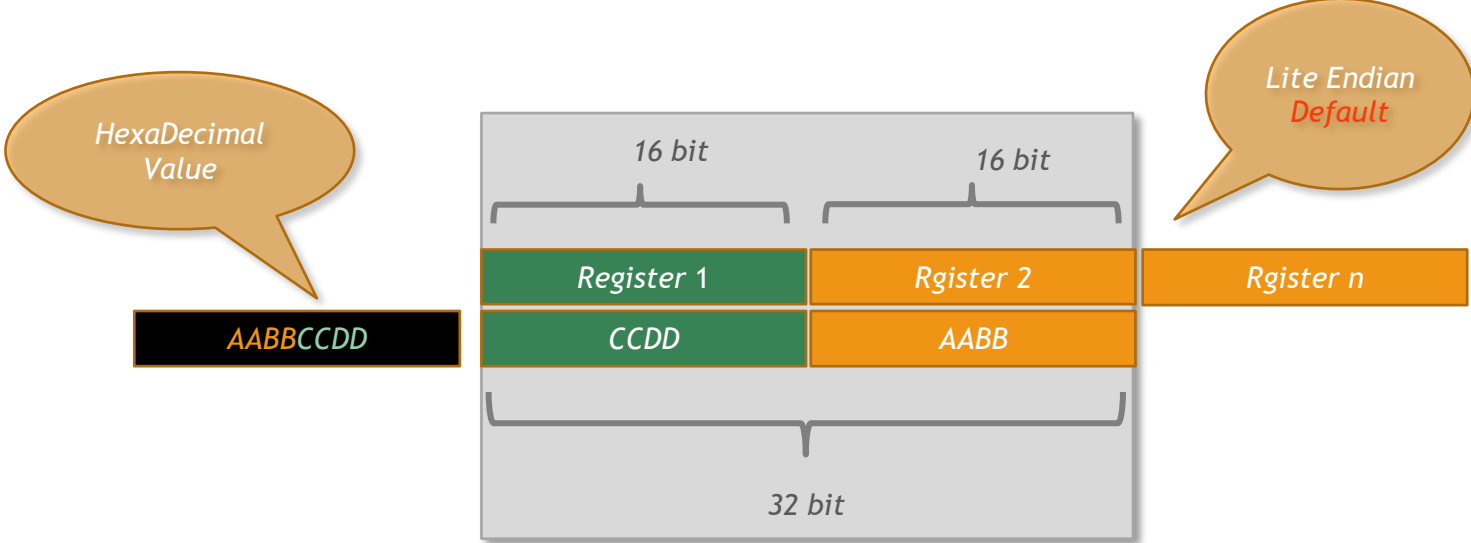
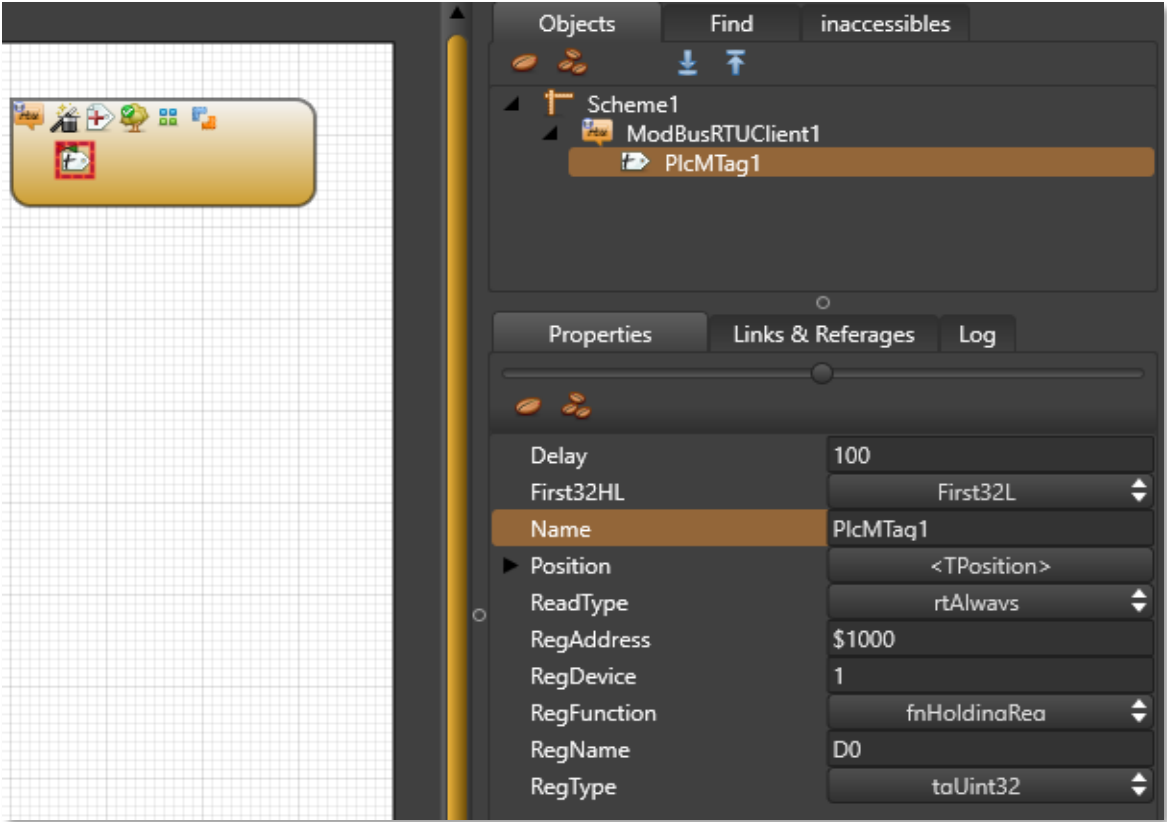
RegtsEndian PLC memory layout method of registers
little endan
Big Endian

RegAddress Register Address
\$A00 or xA00 or 0xA00 Hexadecimal
1000 Decimal

RegDevice Device "PLC" Addr. For RTU

RegName For information

Delay Delay (ms.)
for ReadType rtAfterWriteOrReadDelay or rtAfterWriteOrDelayRead or rtReadDelay or rtDelayRead



Example

